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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Pages Number: 350 Publisher: Tsinghua University. Pub. Date :2010-02-01 version 2. Abalone can enter. Zhao Nianjiang eds this digital logic circuit design (second edition) from the basics of digital circuits starting to introduce a few system and coding. logic algebra. gates. combinational logic. sequential logic. hardware description language (VHDL). programmable devices (PLD, CPLD, HDPLD and FPGA). in-system programming technique (ISP) and EDA technology. design. etc. the content. VHDL hardware description language used to describe the circuit design. Each chapter has a summary at the end of chapter exercises along with a number of questions and thinking. The final chapter also describes MAXPLUS and QUARTUS to use the software and related pilot projects. Provide all the content of the PPT lesson plans. Digital logic circuit design (second edition) This book can serve as institutions of higher learning computer. communications. electronic information. automation and other professional Digital Logic course materials. but also as a reference for relevant technical staff. Contents: Chapter 1 Coding 1.1 Digital system with digital systems in binary number system 1.1.1 number conversion system 1.1.2 1.2 1.2.1 digital encoding system with symbol-digit code that the binary coded decimal numbers 1.2.2 1.2.3 Reliability 1.2.4 encoding character encoding 1.3 Summary 1.4 Exercises Chapter 2 and thinking problems digital signal gate 2.1-based 2.1.1 2.1.2 logic level pulse signal with positive and negative logic switching characteristics of semiconductor devices 2.2 2.2 2.2.2 .1 switching characteristics of diode switching characteristics 2.2.3 MOS tube transistor switching characteristics of 2.3 and 2.3.1 basic logic gates doors. or doors and composite doors 2.3.3 2.3.2 non-door three-state gate and transmission gate 2.4 TTL integrated digital IC gate 2.4.1 Classification 2.4.3 2.4.2 TTL NAND gate with open-collector NAND gate 2.4.4 TTL Caution...



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